



## The Essentials of Computer Organization and Architecture 2<sup>nd</sup> Edition

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# Errata (2<sup>nd</sup> Printing)

*To confirm you have the first printing, see page ii for the following:*

**Printed in the United States of America**  
**10 09 08 07 06 10 9 8 7 6 5 4 3 2**

As errors are found in the textbook, they will be added to this list. The list will be updated as necessary. If you find an error, please send it to [ecoa@jbpub.com](mailto:ecoa@jbpub.com).

### Symbols Used

ti = ith line from top  
bi = ith line from bottom  
Fi = Figure i  
X → Y = replace X with Y  
Ti = Table i  
Pi = Problem i  
Ei = Example i

### Format

Page # Location: Correction  
Strikethrough: Correction/modification in errata

### June 2006 List

5 T1.1: 1 quintillionth =  $10^{+18}$  → 1 quintillionth =  $10^{-18}$

52 t7:  $167 + 947 = 114$  →  $167 + 947 = 1114$

140 t15: function, d → function,  $\delta$

## August 2006 List

211 E4.1: 10A Jump Loop → 10F Jump Loop

237 P6: 256KB × 8 RAM chips → 256K × 8 RAM chips

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## September 2006 List

216 t18:  $P_0P_2T_1$ : MBR ← M[MAR] →  $P_3P_4T_1$ : MBR ← M[MAR]

216 b12: At clock cycle  $C_1$ , all signals except  $P_0$ ,  $P_2$ , and  $T_1$  are →  
At clock cycle  $C_1$ , all signals except  $P_3$ ,  $P_4$ , and  $T_1$  are

217 F4.16: In clock cycle  $C_1$ , signals  $P_3$ ,  $P_4$ , and  $T_1$  should be high, nothing else

355 b6: spinning the disk faster → spinning the disk slower

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